

**Amendments to the Specification**

Please replace the paragraph beginning at page 4, line 13 with the following **rewritten** paragraph:

-- A method to reduce the system clock frequency (  $F_{clk}$  ) requirements is to use a random row access start with programmable line lengths. This scheme would allow systems that did not require the utilization of all pixels the ability to lower system clock and D/A requirements. FIG. 2 illustrates a row address selector 24 coupled to the imager 18 and a controller 23 coupled to the row address selector 24 and the s/h buffer 22 that could be programmed to randomly access a starting row in a liquid crystal display imager and selectively address rows having active video and avoiding addressing rows having all or substantially all unused pixels. The s/h buffer 22 is coupled to the controller 23 to enable the controller 23 to detect if a row of pixels buffered in s/h buffer 22 has all unused pixels.--